

10

Fig. 1

## AUS920010940US1 2/4

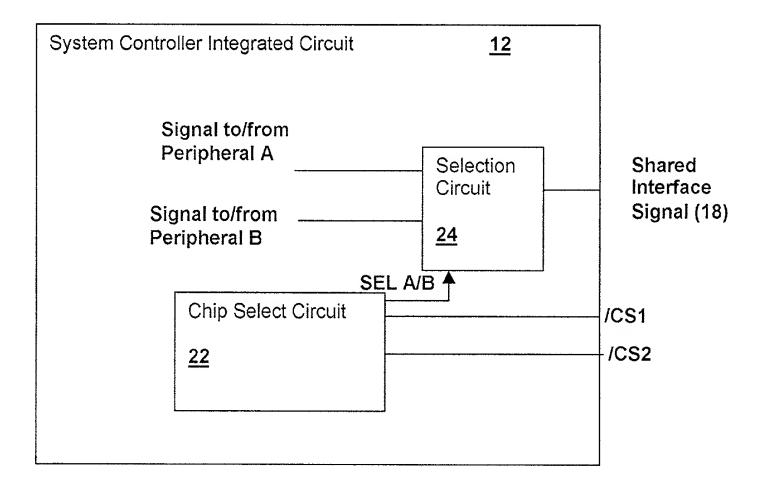
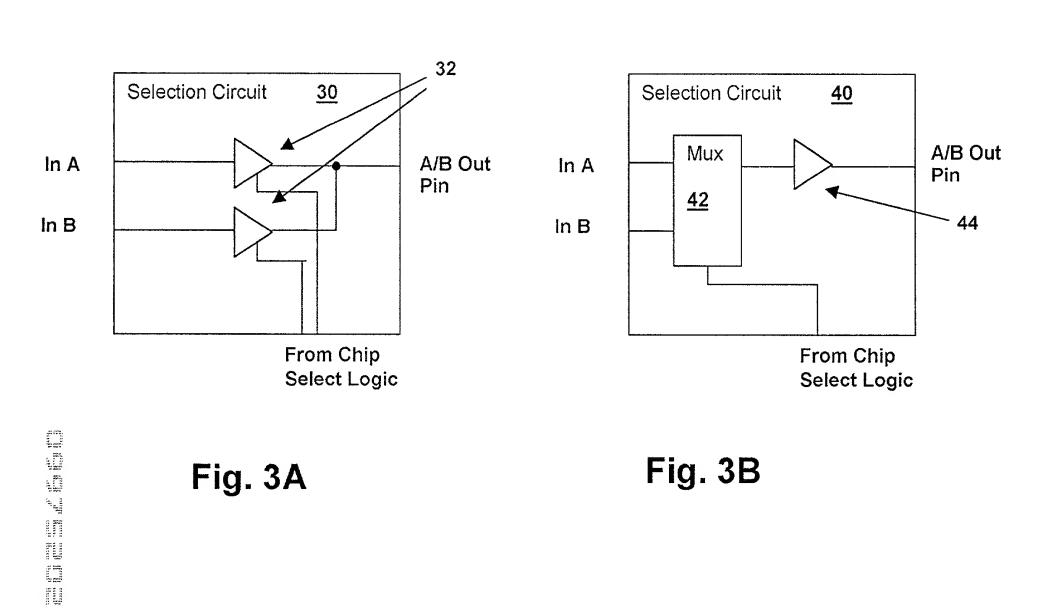


Fig. 2



62 52 Selection Circuit <u>60</u> Selection Circuit <u>50</u> . !!= ln A Bi-Di A Bi-Di A/B Out A / In B Bi-Di B <u>56</u> Out B 64 - 54 g From Chip From Chip Select Logic Select Logic

Fig. 3C

Fig. 3D

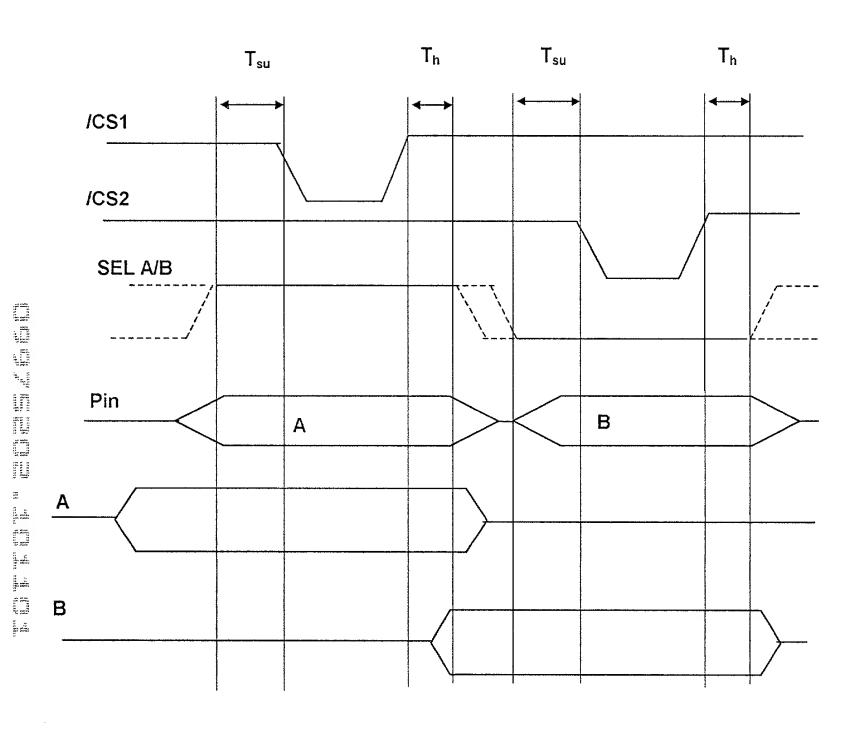


Fig. 4